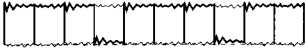
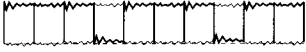
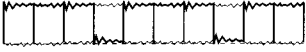
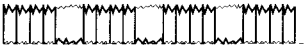



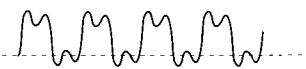


Comp	Name (pin) Circ /IC	Scope	Freq.	Data H/L/A	Description
18	BA10/BA10	Unstable 	635 kHz	A	Buffered Address
19	BA09/BA9	Unstable 	635 kHz	A	Buffered Address
20	BA08/BA8	Unstable 	635 kHz	A	Buffered Address
21	BA07/BA7	Unstable 	616 kHz	A	Buffered Address
22	BA06/BA6	Unstable 	616 kHz	A	Buffered Address
23	BA05/BA5	Unstable 	616 kHz	A	Buffered Address
24	BA04/BA4	Unstable	590 kHz	A	Buffered Address
25	BA03/BA3	Unstable	599 kHz	A	Buffered Address
26	BA02/BA2	Unstable	599 kHz	A	Buffered Address
27	BA01/BA1	Unstable	599 kHz	A	Buffered Address
28	BA00/BA0	Unstable 	624 kHz	A	Buffered Address
29	CEN/ROM0SELN	1	0	H/Rom 0 Select Not
30	A15/ROM1SELN	1	0	H	Address 15/Rom 1 Select Not
31	UPCLOCK		12.5 MHz	A	Micro-processor Clock (1201/9)
32	AD00/D0	0	0	O	Address Data/Data Bus I/O 0
33	AD01/D1	0	0	O	Address Data/Data Bus I/O 1
34	AD02/D2	0	0	O	Address Data/Data Bus I/O 2
35	AD03/D3	0	0	O	Address Data/Data Bus I/O 3
36	AD04/D4	0	0	O	Address Data/Data Bus I/O 4
37	AD05/D5	0	0	O	Address Data/Data Bus I/O 5
38	AD06/D6	0	0	O	Address Data/Data Bus I/O 6
39	AD07/D7	0	0	O	Address Data/Data Bus I/O 7
40	AD08/D8	0	0	O	Address Data/Data Bus I/O 8
41	+5VD/VDD	1	0	H	Volt Supply
42	AD09/D9	0	0	O	Address Data/Data Bus I/O 9
43	.../VSS	0	0	L	Volt Supply Ground
44	AD10/A10	0	0	O	Address Data/Data Bus I/O 10
45	AD11/A11	0	0	O	Address Data/Data Bus I/O 11
46	AD12/A12	0	0	O	Address Data/Data Bus I/O 12
47	AD13/13	0	0	O	Address Data/Data Bus I/O 14
48	AD14/A14	0	0	O	Address Data/Data Bus I/O
49	AD15/A15	0	0	O	Address Data/Data Bus I/O 15
50	SYNCRDY /SYNCRDY	0	0	O	Synchronisation ready